

## **4.20 – Module Appendix**

This section contains a series of standards that do not fit into the pattern established for those standards that are included in Sections 4.1 through 4.6. Many of them are complete Module specifications that use the standards contained in these sections and that also use memory devices defined in sections 3.11 of this Standard and that are also more completely defined in the companion Standard JESD–79.

### **4.20.1 – Standard Template for JEDEC Module Standards**

### **4.20.2 – 168 Pin, PC133 SDRAM Registered DIMM Design Specification**

### **4.20.3 – 144 Pin, PC133 SDRAM Unbuffered SO–DIMM, Reference Design Specification**

### **4.20.4 – 184 Pin, PC1800/2100 DDR SDRAM Registered DIMM Design Specification**

### **4.20.5 – 184 Pin, PC1800/2100 DDR SDRAM Unbuffered DIMM Design Specification**

### **4.20.6 – 200 Pin, PC2700 DDR SDRAM Unbuffered SO–DIMM Reference Design Specification**